

Notice of References Cited	Application/Control No. 10/078,180		Applicant(s)/Patent Under Reexamination MORROW, NEIL G.	
	Examiner Thomas J. Cleary		Art Unit 2111	Page 1 of 2

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-2003/0065869 A1	04-2003	Balay et al.	710/314
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"DS92LV222A Two Channel Bus LVDS MUXed Repeater." National Semiconductor. May 1998.
	V	"MD904 to MD920 1/2 to 2 1/2 MByte Multibank DRAM (MDRAM) 128Kx32 to 656Kx32." MoSys Inc. DS01-2.4-2/21/97. 1996.
	W	"Low Pin Count (LPC) Interface Specification." Revision 1.0. Intel. 9-Sep 1997.
	X	Mackey, Janice. "New Bus LVDS Family Offers Superior Combination of High Speed, Low Power, Low Cost, and Low Noise/EMI." National Semiconductor. 25-May 1998. Retrieved from Internet 8-Nov 2004. < http://www.national.com/news/item/0,1735,294,00.html >.

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

Notice of References Cited	Application/Control No. 10/078,180	Applicant(s)/Patent Under Reexamination MORROW, NEIL G.	
	Examiner Thomas J. Cleary	Art Unit 2111	Page 2 of 2

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"LSI53C140 SCSI Bus Expander." LSI Logic. S20003. Dec 2000.
	V	"LSI53C120 SCSI HVD Bus Expander." LSI Logic. S20042. Mar 2001.
	W	"Bus repeaters improve signal integrity." Electronicstalk. 29-Jan 2002. Retrieved from Internet 8-Nov 2004. < http://www.electronicstalk.com/news/fai/fai159.html >
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.